



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Soichi Kobayashi et al.

Application No.: 10/632,928

Filing Date: August 4, 2003

Title: SEMICONDUCTOR INTEGRATED CIRCUIT CAPABLE OF TESTING WITH SMALL SCALE CIRCUIT CONFIGURATION

Group Art Unit: 2138

Examiner: Saquib J. Siddiqui

Confirmation No.: 4942

PETITION FOR EXTENSION OF TIME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The following extension of time is requested to: the Office Action dated December 29, 2005

one month to April 29, 2006 ☐ \$60.00 (2251) ☒ \$120.00 (1251)

- ☐ The shortened statutory period has been reset by an Advisory Action dated _____.
- ☐ An extension fee in the amount of _____ is enclosed.
- ☐ Charge _____ to Deposit Account No. 02-4800.
- ☒ Charge \$ 120.00 to credit card. Form PTO-2038 is attached.

The Director is hereby authorized to charge any appropriate fees under 37 C.F.R. §§1.16, 1.17 and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in duplicate.

Respectfully submitted,

BUCHANAN INGERSOLL PC

By

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Date: April 28, 2006

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01 FC:1251

120.00 OP

Buchanan Ingersoll PC
ATTORNEYS

Including attorneys from Burns Doane Swecker & Mathis

(8/05)